

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:) Group Art Unit:
Yasuyuki ARAI et al.) Examiner:
Based On: JP 2003-046456)
Filed: February 24, 2004)
For: THIN FILM INTEGRATED CIRCUIT)
DEVICE, IC LABEL, CONTAINER)
COMPRISING THE THIN FILM)
INTEGRATED CIRCUIT,)
MANUFACTURING METHOD OF)
THE THIN FILM INTEGRATED)
CIRCUIT DEVICE,)
MANUFACTURING METHOD OF)
THE CONTAINER, AND)
MANAGEMENT METHOD OF)
PRODUCT HAVING THE)
CONTAINER)

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
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Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published*

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Respectfully submitted,



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PTO/SB/08A (08-00)

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				Application Number	
				Filing Date	
				First Named Inventor	
				Group Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	
				0756-7256	

U.S. PATENT DOCUMENTS						
Examiner Initials [*]	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		2003/0032210		Takayama et al.	02/13/2003	

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		JP	2002-230141			08/16/2002		AB

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